### Amendments to the Specification:

#### IN THE SPECIFICATION

## <u>Page 1:</u>

Please replace the heading and paragraph that precedes paragraph [0001] with the following amended heading and paragraph:

#### CROSS REFERENCE TO RELATED APPLICATIONS

The subject matter of this application is related to application Serial <u>U.S. Patent</u>

Application No. 10/627,977 (attorney docket Attorney Docket No. T2147-908626), filed concurrently herewith in the name of Andrzej WOZNIAK, entitled [[«]]"Procédé et système d'établissement automatique d'un modèle global de simulation d'une architecture[[ »]]" (English title: [[«]]"System and Method for Automatically Generating a Global Simulation Model of An Architecture", and corresponding to French Application No. FR 02/09690<sub>2</sub> filed July 30, 2002, the subject matter of which is hereby incorporated by reference.

Please replace paragraph [0017] with the following amended paragraph:

[0017] The invention will be better understood with the help of the following description of an exemplary embodiment of the method of the invention, in reference to the attached drawings, in which:

[[-]]Fig. 1 represents, in a very schematic form, an exemplary global simulation model;

[[-]]Fig. 2 represents a diagram illustrating the various components of the automatic recognition system and the steps for implementing these components in the method of the invention;

[[-]]Figs. 3a through 3c represent various stages in the modeling of a circuit using a mixed model of the HDL (VERILOG or VDHL) type and the HLL (C++) type and [[-]]Figs. 4a through 4c represent various configurations of the global simulation model corresponding to the architecture represented in Fig. 1.

# IN THE ABSTRACT:

Please replace the original Abstract with the accompanying Abstract on a separate sheet.